

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shi-Tron LIN

Group Art Unit: 2822

Application No.: 09/740,017

Examiner: K. Rose

Filed: December 20, 2000

For: TRANSISTOR STRUCTURE

FOR ELECTROSTATIC DISCHARGE PROTECTION

CIRCUIT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

SUBMISSION OF REPLACEMENT DRAWINGS

Subject to the approval of the Examiner, please replace the originally filed drawings in the above-identified application with the replacement drawings filed herewith (Figures 7, 8, 4A - 4C & 5A - 5C). If the replacement drawings for any reason are not in full compliance with the pertinent statutes and regulations, please so advise the undersigned.

If any fees are necessary for the submission of these formal drawings, please charge our Deposit Account No. 06-0916.

Bv:

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Datad:

10/24/04

Richard V) Burgujian

Reg. No. 31,744